

### **AMENDMENTS TO THE CLAIMS**

*The listing of claims will replace all prior versions and listings of claims in the application:*

#### **Listing of Claims:**

1.     **(Currently Amended)**     An integrated post-amplifier and laser driver assembly for use in connection with an optical receiver and an optical transmitter, comprising:
  - a post-amplifier assembly configured for communication with the optical receiver;
  - a laser driver assembly configured for communication with the optical transmitter;
  - a digital control interface in at least indirect communication with the post-amplifier assembly and the laser driver assembly; and
  - means for setting a signal parameter in response to signal parameter programming instructions, the signal parameter corresponding to a signal associated with at least one of: the post-amplifier assembly; and, the laser driver assembly, wherein the signal parameter includes polarity.
2.     **(Original)**     The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the post-amplifier assembly and laser driver assembly are integrated together in a single IC.
3.     **(Original)**     The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the means for setting a signal parameter enables dynamic control of one or more signal parameters.
4.     **(Currently Amended)**     The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the means for setting a signal parameter operates in response to signal

parameter programming instructions received, at least indirectly, from ~~one of: a user; and,~~ an algorithm encoded within the integrated post-amplifier and laser driver assembly.

5. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the means for setting a signal parameter enables implementation of a signal parameter change that corresponds to a change in thermal conditions associated with the integrated post-amplifier and laser driver assembly.

6. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the signal to which the signal parameter corresponds is selected from the group consisting of: control signals; data signals; power signals; and, monitor signals.

7. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the digital control interface comprises a serial digital interface.

8. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 1, further comprising a means for controlling signal polarity arranged for operation with respect to a data signal associated with the integrated post-amplifier and laser driver assembly.

9. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 1, further comprising a feedback loop configured and arranged for operation with respect to the signal to which the signal parameter corresponds.

10. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the integrated post-amplifier and laser driver assembly is compatible with a plurality of protocols and line rates.

11. **(Original)** In a high-speed data communications system that includes an optical receiver and an optical transmitter, an integrated post-amplifier and laser driver assembly, comprising:

a post-amplifier assembly configured for communication with the optical receiver;

a laser driver assembly configured for communication with the optical transmitter and implemented together with the post-amplifier assembly in a single IC;

a digital control interface;

a glue logic module in communication with the digital control interface and in at least indirect communication with the post-amplifier assembly and the laser driver assembly; and

a digital to analog converter configured to receive signal parameter programming instructions concerning the setting of a signal parameter, the digital to analog converter being in communication with the glue logic module and at least one of: the post-amplifier assembly; and, the laser driver assembly.

12. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the digital to analog converter is configured to receive signal parameter programming instructions from at least one of: a user; and, an algorithm encoded within the integrated post-amplifier and laser driver assembly.

13. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal parameter programming instructions concern LOS assert and deassert thresholds.

14. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal parameter is selected from the group consisting of: signal rise time; signal

polarity; signal assert threshold; signal deassert threshold; signal fall time; signal amplitude; and, signal power.

15.     **(Original)**     The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal to which the signal parameter corresponds is selected from the group consisting of: control signals; data signals; power signals; and, monitor signals.

16.     **(Original)**     The integrated post-amplifier and laser driver assembly as recited in claim 11, further comprising an LOS circuit in at least indirect communication with the optical receiver and the digital control interface.

17.     **(Original)**     The integrated post-amplifier and laser driver assembly as recited in claim 11, further comprising a feedback loop configured and arranged for operation with respect to the signal to which the signal parameter corresponds.

18.     **(Original)**     The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the post-amplifier assembly includes an amplifier having a data signal polarity control stage.

19.     **(Original)**     The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the integrated post-amplifier and laser driver assembly is compatible with a plurality of system protocols and line rates.

20. **(Currently Amended)** An optical transceiver, comprising:
- an optical transmitter;
  - an optical receiver;
  - a digital IC controller; and
  - an integrated post-amplifier and laser driver assembly in communication with the digital IC controller, the integrated post-amplifier and laser driver assembly being programmable with respect to at least one signal parameter of at least one signal, and comprising:
    - a post-amplifier assembly configured for communication with the optical receiver;
    - a laser driver assembly configured for communication with the optical transmitter, wherein the post-amplifier assembly and laser driver assembly are integrated together in a single IC; and
    - a digital control interface in communication with the digital IC controller and in at least indirect communication with the post-amplifier assembly and the laser driver assembly.
21. **(Original)** The optical transceiver as recited in claim 20, wherein the integrated post-amplifier and laser driver assembly is configured to receive signal parameter programming instructions from at least one of: a user; and, an algorithm encoded within the optical transceiver.
22. **(Original)** The optical transceiver as recited in claim 20, wherein the at least one signal to which the at least one signal parameter corresponds is selected from the group consisting of: control signals; data signals; power signals; and, monitor signals.

23. **(Original)** The optical transceiver as recited in claim 20, wherein the at least one signal parameter is selected from the group consisting of: signal rise time; signal polarity; signal assert threshold; signal deassert threshold; signal fall time; signal amplitude; and, signal power.

24. **(Original)** The optical transceiver as recited in claim 20, further comprising:  
a glue logic module in communication with the digital control interface; and  
at least one digital to analog converter configured to receive signal parameter programming instructions, the at least one digital to analog converter being in communication with the glue logic module and at least one of: the post-amplifier assembly; and, the laser driver assembly.

25. **(Original)** The optical transceiver as recited in claim 24, wherein the at least one digital to analog converter is configured to receive the signal parameter programming instructions from at least one of: a user; and, an algorithm internally encoded in the integrated post-amplifier and laser driver assembly.

26. **(Original)** The optical transceiver as recited in claim 20, further comprising an LOS circuit in at least indirect communication with the optical receiver and the digital IC controller.

27. **(Original)** The optical transceiver as recited in claim 20, further comprising a feedback loop configured and arranged for operation with respect to the at least one signal to which the at least one signal parameter corresponds.

28. **(Original)** The optical transceiver as recited in claim 20, wherein the post-amplifier assembly includes an amplifier having a data signal polarity control stage.

29. **(Canceled)**

30. **(Original)** The optical transceiver as recited in claim 20, wherein the digital IC controller includes an I2C bus through which signal parameter programming instructions can be passed.

31. **(Original)** The optical transceiver as recited in claim 20, wherein the optical receiver comprises a 'receive' optical subassembly.

32. **(Original)** The optical transceiver as recited in claim 31, wherein the 'receive' optical subassembly comprises:

a transimpedance amplifier; and

a photodiode in communication with the transimpedance amplifier.

33. **(Original)** The optical transceiver as recited in claim 20, wherein the optical transmitter comprises a 'transmit' optical subassembly.

34. **(Original)** The optical transceiver as recited in claim 33, wherein the 'transmit' optical subassembly comprises:

a laser; and

a photodiode in communication with the laser.

35. **(Original)** The optical transceiver as recited in claim 34, wherein the laser comprises a VCSEL.

36. **(Original)** The optical transceiver as recited in claim 20, wherein the optical transceiver is compatible with a plurality of system protocols and line rates.

37. **(Original)** The optical transceiver as recited in claim 20, wherein the integrated post-amplifier and laser driver assembly is configured so that programming implemented with respect to the at least one signal parameter of at least one signal is based upon at least one of: system line rate; and, system protocol.



38. **(Original)** An integrated post-amplifier and laser driver assembly for use in connection with an optical receiver and an optical transmitter, comprising:

a post-amplifier assembly configured for communication with the optical receiver;

a laser driver assembly configured for communication with the optical transmitter;

a digital control interface;

an LOS circuit in at least indirect communication with the optical receiver and the digital control interface;

a glue logic module in communication with the digital control interface; and

a digital to analog converter configured to receive signal parameter programming instructions concerning the setting of LOS assert and deassert thresholds, the digital to analog converter being in communication with the glue logic module and at least one of: the post-amplifier assembly; and, the laser driver assembly.

39. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 38, wherein the digital to analog converter is configured to receive signal parameter programming instructions from at least one of: a user; and, an algorithm encoded within the integrated post-amplifier and laser driver assembly.

40. **(Original)** The integrated post-amplifier and laser driver assembly as recited in claim 38, wherein the LOS circuit is configured to receive first and second signals, the first signal corresponding to strength of an optical signal received at the integrated post-amplifier and laser driver, and the second signal being received from the digital to analog converter and corresponding to an LOS assert threshold if no LOS signal has been asserted by the LOS circuit, and the second signal corresponding to an LOS deassert threshold if an LOS signal has been asserted by the LOS circuit, the LOS circuit asserting or deasserting the LOS signal as dictated by a relationship collectively defined by the first and second signals.

41.-46 **(Canceled)**

47. **(New)** The integrated post-amplifier and laser driver assembly as recited in claim 1, wherein the means for setting a signal parameter operates in response to signal parameter programming instructions received, at least indirectly, from a user.

48. **(New)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal parameter includes a signal rise time.

49. **(New)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal parameter includes a signal polarity.

50. **(New)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal parameter includes a signal assert threshold and a signal deassert threshold.

51. **(New)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal parameter includes a signal fall time.

52. **(New)** The integrated post-amplifier and laser driver assembly as recited in claim 11, wherein the signal parameter includes signal amplitude.

53. **(New)** The optical transceiver as recited in claim 20, wherein the at least one signal parameter includes signal rise time; signal polarity; signal assert threshold; signal deassert threshold; signal fall time; signal amplitude; and, signal power.

54. **(New)** The computer program product as recited in claim 42, wherein the predefined event comprises a change in thermal conditions associated with the optical transceiver.

55. **(New)** The computer program product as recited in claim 42, wherein the predefined event comprises a change in protocol.